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**APPLICANTS**

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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/JP03/07457 06/12/2003

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***JAPAN 2002-177970 06/19/2002  
JAPAN 2002-177971 06/19/2002**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

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Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature _____ Initials _____				

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**TITLE**

Chip resistor having low resistance and its producing method

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